

EUROPEAN PATENT OFFICE

Patent Abstracts of Japan

PUBLICATION NUMBER : 2001186581
PUBLICATION DATE : 06-07-01

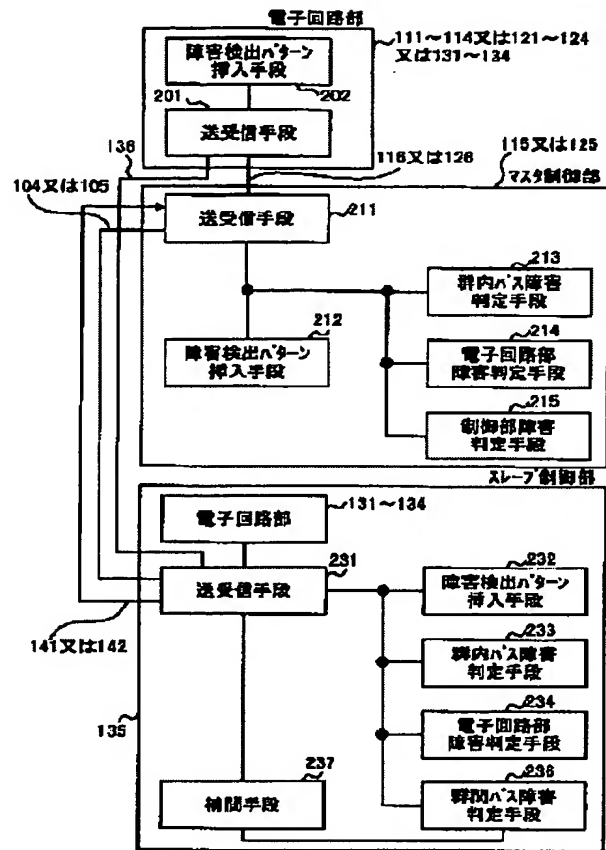
APPLICATION DATE : 27-12-99
APPLICATION NUMBER : 11369944

APPLICANT : MATSUSHITA ELECTRIC IND CO LTD;

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INT.CL. : H04Q 11/04 H04B 3/46

TITLE : FAULT DETECTOR AND FAULT
DETECTION METHOD



ABSTRACT : **PROBLEM TO BE SOLVED:** To enhance the traffic of actual communication data such as user data, to easily extend communication channel and to specify fault location by restraining the number of fault detection slots required for detecting the fault as much as possible, even when the number of communication paths, such as a bus, is extended.

SOLUTION: This fault detector has respective electronic circuit sections 111-114, which are interconnected by an in-group bus 116 which frame data having one fault detection slot for every frame are transmitted, and a control section 115 that controls the electronic circuit sections 111-114 via the bus 116. An insertion means 212 inserts prescribed data to the slot to attain a prescribed pattern in the case of connection of the fault detection slot, the frame data are received by a transmission reception means 201 via the in-group bus 116, looped back to the in-group bus 116, and when the pattern of the reception frame data by the transmission reception means 211 is not a specific pattern, the in-group bus 116 is discriminated as being anomalous.

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